

## CHIRP - Bug # 1627

<b>Status:</b>	Closed	<b>Priority:</b>	Normal
<b>Author:</b>	Jim Unroe	<b>Category:</b>	
<b>Created:</b>	05/11/2014	<b>Assignee:</b>	Jim Unroe
<b>Updated:</b>	07/09/2014	<b>Due date:</b>	
<b>Chirp Version:</b>	daily		
<b>Model affected:</b>	KT-980HP		
<b>Platform:</b>	All		
<b>Subject:</b>	[KT-980HP] Bug with High/Mid/Low TX Power Levels		
<b>Description</b>	The power levels of the Intek KT-980HP are not fully implemented		

### Associated revisions

Revision 2219:b19589f454da - 05/11/2014 08:42 am - Jim Unroe

[KT-980HP] Bug with High/Mid/Low TX Power Levels

Edit 'settings' structure

'lowpower' expanded to 2 bits to support 3 power levels

Edit 'vfoa' and 'vfob' structures

Add 'txpower3' to support relocated parameters

Add 'UV5R\_POWER\_LEVELS'

Edit 'def get\_memory(self, number):'

Updated code for 'mem.power'

Edit 'def set\_memory(self, number):'

Updated code for '\_mem.lowpower'

Edit "Work Mode Settings" menu

Updated settings for VFO A and VFO B TX Power Level settings

Updated IntekKT980Radio class

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### History

#1 - 07/09/2014 03:05 am - Jim Unroe

- Status changed from New to Closed

- % Done changed from 90 to 100

Patch is now included in current daily build