

CHIRP - Feature # 1863

Status:	New	Priority:	Normal
Author:	Jim Unroe	Category:	
Created:	08/23/2014	Assignee:	Jim Unroe
Updated:	08/23/2014	Due date:	
Chirp Version:	daily		
Model affected:	KG-UV6X		
Subject:	[KG-UV6]Improve Support for the Powerwerx Wouxun KG-UV6X		
Description	<p>Currently CHIRP treats the KG-UV6D and KG-UV6X the being the same. They are not the same and CHIRP should be updated to support the differences.</p> <p>PONMSG: This is only supported on the KG-UV6D. The same byte that contains the setting for the KG-UV6D is set to 0xFF in the KG-UV6X since it isn't used. This causes an out-of-range error when read into CHIRP since it doesn't match any of the values CHIRP expects to see for the KG-UV6D.</p> <p>BANDWIDTH: This is a single setting in the KG-UV6D for each memory and each VFO. It controls the BANDWIDTH setting for both RX and TX. The KG-UV6X on the other hand, has a separate setting for RX BANDWIDTH and TX BANDWIDTH. So it is currently impossible to set the TX BANDWIDTH of the KG-UV6X.</p>		

Associated revisions

Revision 2250:facf7dacf48d - 08/23/2014 03:16 pm - Jim Unroe

[KG-UV6]Improve Support for the Powerwerx Wouxun KG-UV6X

Add "iswidex" to structures to accomodate the KG-UV6X's TX Bandwidth setting

Add "Mode TX" to the mem.extra settings to allow editing each memory's TX Bandwidth

Add a 4th setting, "N/A(KG-UV6X)", to the options for "ponmsg" to prevent out-of-range values

#1863

History

#1 - 08/23/2014 04:00 pm - Jim Unroe

- % Done changed from 0 to 100

Patch submitted